

## strongSwan - Bug #66

### patch for alignment buffer on xscale ARM processor

24.10.2008 01:22 - Martin Willi

<b>Status:</b>	Closed	<b>Start date:</b>	
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Martin Willi	<b>Estimated time:</b>	0.00 hour
<b>Category:</b>	charon	<b>Resolution:</b>	
<b>Target version:</b>	4.2.9		
<b>Affected version:</b>	5.9.2		
<b>Description</b>			
The "request" buffers used in the kernel_interface.c file may not always be word-aligned on the xscale big-endian arm processor. When this happens the fields are not read back properly when passed to the kernel. As a result some kernel operations such as adding SAD or SPD entries will fail. The attached patch aligns all the buffers on 32bit boundaries.			

#### History

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**#1 - 24.10.2008 10:20 - Martin Willi**

- Status changed from New to Assigned

**#2 - 14.11.2008 15:35 - Martin Willi**

- Status changed from Assigned to Closed

- Affected version set to fixed

Fixed in r4660.

I think `attribute((align(4)))` is sufficient, as the align attribute takes bytes, not bits.

#### Files

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500-alignment.patch	4.49 KB	23.10.2008	Martin Willi
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